

CENTURY FASTENERS CORP. QUALITY TERMS AND CONDITIONS

CLAUSE NUMBER: 01000-SQ-NG T&C-100

rev.000

EFFECTIVE: 05/31/2024

Lot Acceptance Test (LAT) - Amraam

Unless otherwise specified on the purchase order, the seller shall provide lot acceptance test (LAT) data in accordance with the applicable contract requirements with each shipment. The test data shall contain information such that the test data is traceable to the hardware tested and being presented for acceptance. Appropriate information such as part number, revision letter, purchase order number, supplier name, lot number/code, etc., shall be used. Unless otherwise stated, LAT samples and test data shall not be shipped in advance of the hardware. The retention of the LAT data shall not modify or limit any representations, warranties, or commitments made elsewhere herein, or in any way affect the obligation of the seller to perform strictly in accordance with the provisions of the purchase order. Special instructions for chip manufacturers: in all cases, sample selection for LAT shall be random sample selected for the total population of chips comprising a single inspection lot. The actual sample size and any special criteria shall be in accordance with the associated specifications. In the case of an inspection lot comprised of more than one wafer, it is not acceptable to select all samples for a single wafer. Note: the use of a random number table is encouraged. Wafers from the same wafer/manufacturing lot which are not included in the population sample are not considered part of the qualified lot, i.e., they require random sampling for LAT as a new inspection lot.